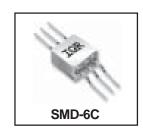
# International IOR Rectifier

# **Ultra Low Dropout, 10A Adjustable Positive Linear Regulator Surface Mount (SMD-6C)**

# **OM7581SM** 5962 - 0323702MXA

## **Product Summary**

Part Number	Output Voltage	Current	Dropout
OM7581SM	+1.8V to +5.5V	10A	0.66V



## **Description**

The OM7581SM is a 10A, ultra low dropout, adjustable linear regulator specifically designed for low voltage, high current applications. Housed in a hermetic package, the dropout of this device is 660mV at full load and as low a 100mV at light loads. The low dropout is achieved by an additional low current input voltage. This unit is ideally suited for military/defense, commercial aircraft, industrial control and other harsh environments where a hermetically sealed package is required.

#### Features:

- Dropout Voltage of 660mV at Full Load
- Dropout Voltage of 100mV at Light Loads
- Fast Transient Response
- Adjustable Output: 1.8 to 5.5V
- Remote Sense
- Hermetic SMD-6C Package ensures High Reliability

## **Absolute Maximum Ratings**

Parameter	Symbol	Value	Units	
Output Currrent	I <sub>O</sub>	10	Α	
Power Input Voltage	$V_{PWR}$	6.0	V	
Control Input Voltage	V <sub>CTRL</sub>	13		
Power Dissipation @ Tc = 25°C	$P_{D}$	20	W	
Thermal Resistance, Junction to Case	$R_{ heta JC}$	5.0	°C/W	
Operating Junction Temperature Range	TJ	-55 to +125		
Storage Temperature Range	T <sub>STG</sub>	-65 to +150	°C	
Lead Temperature Soldering (10second maximum)	T <sub>L</sub>	300		

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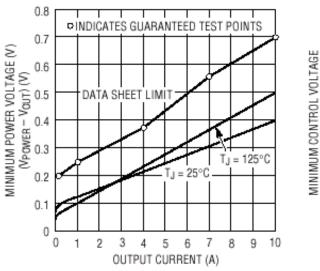
# Electrical Characteristics @T<sub>A</sub> = 25°C (Unless Otherwise Specified)

Parameter	Test Conditions	Min.	Тур.	Max.	Units			
Reference Voltage	$V_{CTRL} = 2.7V, V_{PWR} = 2.0V, I_{LOAD} = 10mA$	1.243	1.250	1.257	V			
V <sub>ADJ</sub> = 0V	$V_{CTRL}$ =2.7 to 12V, $V_{PWR}$ =1.75V to 5.5V, $I_{LOAD}$ =10mA to 10A $\oplus$	1.237	1.250	1.263	V			
Line Regulation	$V_{CTRL}$ = 2.5 to 12V, $V_{PWR}$ = 3.0V to 5.5V, $I_{LOAD}$ = 1.0mA ①	-	1.0	3.0	mV			
Load Regulation	$V_{CTRL}$ = 2.75V, $V_{PWR}$ = 2.75V, $I_{LOAD}$ = 10mA to 10A ①		1.0	10	IIIV			
Minimum Load Current	V <sub>CTRL</sub> = 5.0V, V <sub>PWR</sub> = 3.3V, V <sub>ADJ</sub> = 0V ⊕②	-	3.0	10				
Ground Pin Current	$V_{CTRL} = 5.0V$ , $V_{PWR} = 3.3V$ , $I_{LOAD} = 0$ mA ①		6.0	10	mΑ			
Control Pin Current 3	V <sub>CTRL</sub> = 2.75V, V <sub>PWR</sub> = 2.75V, I <sub>LOAD</sub> = 10A ①			170				
Adjust Pin Current	$V_{CTRL} = 2.75V, V_{PWR} = 2.05V, I_{LOAD} = 10mA$		50	120	μΑ			
V <sub>ADJ</sub> = 0V								
Disale Delegation	$V_{CTRL} = V_{PWR} = 5.0V \text{ (AVG)}, V_{RIPPLE} = 1.0V_{P-P}, f = 120Hz$	60	80		dB			
Ripple Rejection	$I_{OUT} = 4.0A TJ = 25^{\circ}C$							
Current Limit	$V_{CTRL}$ = 2.75V, $V_{PWR}$ = 2.75V, $\Delta V_{OUT}$ = 100mV, $TJ_{=}$ 25°C	10.1	11		^			
Current Limit	$V_{CTRL}$ =2.75V, $V_{PWR}$ =2.75V, $\Delta V_{OUT}$ =100mV, TJ=-55°C & TJ=+125°C	10.1	-	-	Α			
Minimum V <sub>CONTROL</sub>	$V_{PWR} = 3.3V$ , $I_{LOAD} = 10A$ ①			1.35				
	V <sub>CTRL</sub> = 2.75V, I <sub>LOAD</sub> = 10A, TJ = 25°C			0.66	V			
Minimum V <sub>PWR</sub>	V <sub>CTRL</sub> = 2.75V, I <sub>LOAD</sub> = 10A, TJ = 125°C			0.85	v			
	V <sub>CTRL</sub> = 2.75V, I <sub>LOAD</sub> =10A, TJ = -55°C			0.85	. ]			
Thermal Regulation	$V_{PWR}$ = 5.0V, $I_{O}$ = 10A, $P_{D} \ge$ 20W, pulse width = 30ms			0.02	%/W			
	Dropout is caused by either minimum control voltage or minimum power voltage. Both							
Dropout Voltage	parameters are specified with respect to the output voltage. The specifications represent the							
	minimum input/output voltage required to maintain 1% regulation.							

#### **Footnotes**

- ①- Denotes specifications which apply over the full operating temperature range.
- ②- The minimum load current is minimum current required to maintain regulation. Normally the current in the resistor divider used to set the output voltage is selected to meet the minimum load current requirement.
- ③- The control pin current is the drive current required for the output transistor. The control pin current is approximately 0.01% output current. The minimum value is equal to quiescent current of the device.

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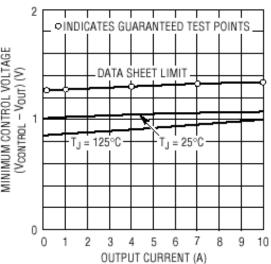


Fig 1: Typical Power Voltage Vs Output Current

Fig 2: Typical Control Voltage Vs Output Current

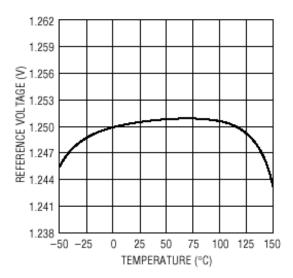


Fig 3: Typical Reference Voltage Vs Temperature

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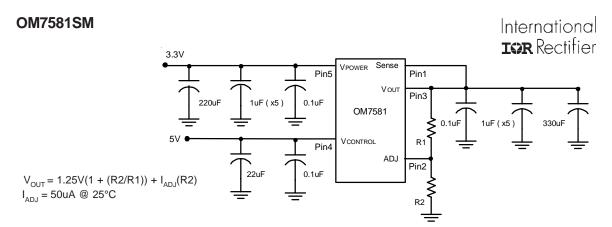
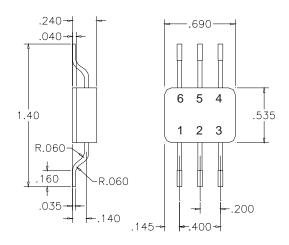


Fig 5: Typical Application

#### **Layout Consideration**

It is recommended that output capacitors be located as close as possible to the  $V_{OUT}$  terminal of the device to prevent any high frequency oscillation that may result due to excessive stray inductance. Specifications for capacitors:  $330\mu F$  Tantalum Low ESR,  $220\mu F$  Electrolytic,  $22\mu F$  Electrolytic

## Case Outline and Dimensions — SMD-6C



### Pin Assignment

Pin#	Pin Description	
1	ADJUST	
2	N/C	
3	SENSE	
4	V <sub>CTRL</sub>	
5	V <sub>OUT</sub>	
6	$V_{PWR}$	

## **Part Numbering Nomenclature**

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<u>OM</u>	<u>7581</u>	<u>S</u>	<u>M</u>	<u>X</u>	
Omnirel	Device	S=Isolated	Package	Screening	



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